

**UNIVERSITY OF VIRGINIA  
BOARD OF VISITORS**

**Meeting of the Ad Hoc  
Committee on Research**

**June 7, 2018**

**AD HOC COMMITTEE ON RESEARCH**

**Thursday, June 7, 2018  
8:15 - 8:45 a.m.  
Board Room, The Rotunda**

**Committee Members:**

Jeffrey C. Walker, Chair  
L.D. Britt, M.D.  
Thomas A. DePasquale  
Maurice A. Jones

Babur B. Lateef, M.D.  
Frank M. Conner III, Ex-officio  
Margaret F. Riley, Faculty Member

**AGENDA**

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<b>I. OPENING COMMENTS</b> (Mr. Walker)	1
<b>II. RESEARCHER PERSPECTIVE</b> (Mr. Ramasubramanian to introduce Mr. Kevin Skadron; Mr. Skadron to report)	2
<b>III. REPORT ON ENHANCING RESEARCH</b> (Mr. Ramasubramanian)	3

**UNIVERSITY OF VIRGINIA  
BOARD OF VISITORS AGENDA ITEM SUMMARY**

**BOARD MEETING:** June 7, 2018

**COMMITTEE:** Research

**AGENDA ITEM:** I. Opening Comments

**ACTION REQUIRED:** None

**DISCUSSION:** The Committee Chair, Mr. Jeff Walker, will make opening comments.

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**AGENDA ITEM:** II. Researcher Perspective

**ACTION REQUIRED:** None

**DISCUSSION:** Mr. Kevin Skadron has been on the faculty at the University of Virginia since 1999. He received his B.S. in Electrical and Computer Engineering and B.A. in Economics from Rice University in 1994, and his Ph.D. in Computer Science from Princeton University in 1999. He spent the 2007-2008 academic year on sabbatical at NVIDIA Research. He became department chair in 2012. He also helped found and serves as director for the UVA Center for Automata Processing (CAP).

Mr. Skadron's research is focused on computer architecture and computational science, especially pertaining to multi-core and multi-threaded chip architectures and novel heterogeneous processor organizations; automata processing; architectures for managing power, temperature, and reliability; and architectural modeling and simulation methodology.

His work is currently supported by the National Science Foundation, DARPA MTO (PERFECT program), C-FAR, one of six centers of STARnet, a Semiconductor Research Corporation program sponsored by MARCO and DARPA; the Virginia CIT CRCF program; and equipment donations from Micron, Xilinx, NVIDIA, and AMD. The UVA CAP is supported in part by Micron.

Mr. Skadron recently was selected to establish a \$27.5 million national center to remove a bottleneck built into computer systems 70 years ago that is increasingly hindering technological advances today. The school's new Center for Research in Intelligent Storage and Processing in Memory, or CRISP, will bring together researchers from eight universities to remove the separation between memories that store data and processors that operate on the data.

Mr. Skadron will discuss his research focusing on computation in memory.

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**AGENDA ITEM:** III. Report on Enhancing Research

**ACTION REQUIRED:** None

**DISCUSSION:** Mr. Ramasubramanian will give a brief report on the progress of the 3Cavaliers program, research administration restructuring, and strategies for research growth.